

AMENDMENTS TO THE SPECIFICATION:

Please amend page 3, paragraph 1, as follows:

A block circuit diagram of a QAM receiver is shown in Figure 1. The QAM receiver consists of an in-phase signal branch and a quadrature-phase signal branch. A mixer MI is situated in the in-phase signal branch and a mixer MQ in the quadrature-phase signal branch. These two mixers MI and MQ convert the received signal ES, which is divided on both signal branches, into the base band. The reference frequency for the two mixers MI and MQ is delivered by a voltage-controlled oscillator VCO. The reference frequency signal generated by this voltage-controlled oscillator is fed from one of the two mixers to a 90° phase shifter PS, so that the reference frequencies of the two mixers MI and MQ have a mutual phase offset of 90°. The output signal of mixer MI, which represents the in-phase signal component ZI of a received signal state, and the output signal of mixer MQ, which represents the quadrature-phase signal component ZQ of a received signal state, are fed to a phase error detector PFD. This phase error detector PFD, as described in detail below, includes a signal receiver, a signal generator, and a processor which processes the algorithms described below, generates a phase correction signal S that serves as control signal for the voltage-controlled oscillator VCO.